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The role of arithmetic in datapath design in VLSI design has been increasing in importance over the last several years due to the demand for processors that are smaller, faster, and dissipate less power. Unfortunately, this means that many of these datapaths will be complex both algorithmically and

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Behrooz Parhami's Textook on Computer Arithmetic (2e) Page last updated on 2016 August 17 B. Parhami, Computer Arithmetic: Algorithms and Hardware Designs, 2nd edition, Oxford University Press, New York, 2010. (ISBN 978-0-19-532848-6, 641+xxv pp., 316 figures, 718 problems)

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Aspects regarding VLSI design of arithmetic units 1.2 Motivation Arithmetic units are, among others, core of every data path and addressing unit ... CORDIC (COordinate Rotation Digital Computer) Computer Arithmetic: Principles, Architectures, and VLSI Design 9 3 Number Representations 3.1 Binary Number Systems (BNS)

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Datapath Design of Computer Architecture 1. Presentation on Datapath 2. Datapath A datapath is a collection of functional units, such as arithmetic logic units or multipliers, that perform data processing operations, registers, and buses.

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